PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Dong-Soo YOON

Serial No.: New Application Group Art Unit: Not Yet Assigned

Filed: July 7, 2003

Examiner: Not Yet Assigned

Title: METHOD FOR FABRICATING SEMICONDUCTOR DEVICE

INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner for Patents PO Box 1450, Alexandria, VA 22313

July 7, 2003

Sir:

As a means of complying with the duty of disclosure under 37 CFR §1.56, and in accordance with 37 CFR §§1.97 and 1.98, Applicant(s), through the undersigned attorney, submits this Information Disclosure Statement. The patents, publications or other information submitted herewith are listed on the attached Form PTO-1449 and copies are attached.

In accordance with 37 CFR §1.97(b)(1) or (2), this Information Disclosure Statement is being filed either within three months of the filing date of the above-identified application, or within three months of the date of entry into the national stage of the aboveidentified application as set forth in 37 CFR §1.491. Accordingly, no fee is required.

Respectfully submitted,

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LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT

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U.S. PATENT DOCUMENTS								
*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	INT'L <u>CLASS</u>		FILING DATE (If Appropriate)	
	AA	5,002,730	3/26/91	Fetcenko	<u>C01B</u>	6/00	7/24/89	
	AB	6,511,925	1/28/03	Aronowitz et al.	<u> H01L</u>	21/8238	10/19/01	
	AC	6,498,714	12/24/02	<u>Fujisawa et al.</u>	<u>H01G</u>	4/228	10/04/00	
*****	*****	*****	*****	******	*****	*****	****	
FOREIGN PATENT DOCUMENTS								
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- <u>CLASS</u>	TRANSLATION (YES) (NO)	
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	AE		-	_				
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EXAMINER	7			DATE CONSIDERED				

^{*} EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).